

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	102157	((large adj scale adj integration) LSI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 11:31
L2	43	1 and three adj circuit adj blocks	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 12:12
L3	832	circuit adj block with (switch switching) with (power supply)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 12:16
L4	721	circuit adj block with (switch switching) with power	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 12:15
L5	538	circuit adj block with (switch switching) with supply	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 12:15
L6	344	circuit adj block with (switch switching) with (power adj supply)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 12:16
L7	142	1 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 13:04
L8	5	power adj supplies with (switch switching) with circuit adj block	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/02/14 13:06
L9	5	second adj power adj supply with (switch switching) with circuit adj block	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 13:07
S1	41500	LSI	US-PGPUB; USPAT	OR	ON	2005/02/11 10:16
S2	15614	LSI	JPO	OR	ON	2005/02/10 12:43
S3	570	system adj LSI	JPO	OR	ON	2005/02/10 12:43
S4	10	system adj LSI and circuit adj block	JPO	OR	ON	2005/02/10 13:04
S5	3	system adj large adj scale adj integration	JPO	OR	ON	2005/02/10 13:05
S6	28	system adj large adj scale adj integration	US-PGPUB; USPAT	OR	ON	2005/02/10 13:16
S7	102107	((large adj scale adj integration) LSI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/10 13:17

S8	270	S7 and (test testing tested) with (circuit adj block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/10 16:12
S9	771	S7 and (circuit adj block) with (power voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/10 16:13
S10	545	S7 and (circuit adj block) with (power voltage) and power adj supply	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/10 16:14
S11	215	S7 and (circuit adj block) with (power voltage) and power adj supply and (test testing tested)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/10 16:14
S12	395	(702/57).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 10:37
S13	287	(702/58).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 11:08
S14	280	(702/81).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 11:19
S15	159	(702/82).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 11:26
S16	85	(702/83).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 11:28
S17	283	(702/84).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 11:36
S18	313	(702/108).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 11:47
S19	510	(702/117).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 12:44
S20	211	(702/118).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 12:52
S21	199	(702/119).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 13:00
S22	191	(702/120).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 13:04
S23	102	(702/121).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 13:08
S24	182	(702/123).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 13:16
S25	131	(702/124).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 13:18
S26	66	(702/125).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 13:20
S27	853	(702/182).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 14:11
S28	711	(702/183).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 14:50
S29	302	(257/203).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 15:19
S30	456	(257/207).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 15:56
S31	520	(257/208).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 16:44
S32	244	(324/500).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 16:48

S33	205	(324/527).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 16:51
S34	2700	(324/765).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 17:11
S35	288	S34 and ((large adj scale adj integration) LSI)	US-PGPUB; USPAT	OR	ON	2005/02/11 16:51
S36	2323	(438/14,17).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/02/11 17:14
S37	231	S36 and ((large adj scale adj integration) LSI)	US-PGPUB; USPAT	OR	ON	2005/02/11 17:15
S38	102107	((large adj scale adj integration) LSI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:28
S39	1	S38 and (circuit adj block) with (test adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:31
S40	21	S38 and (circuit adj block) with second adj voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:40
S41	6	S38 and (circuit adj block) same (simultaneous concurrent) with (test testing tested)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:42
S42	18	S38 and (circuit adj block) same (simultaneously concurrently) with (test testing tested)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:47
S43	0	S38 and (circuit adj block) same selectable adj power adj supply	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:47
S44	3	S38 and (test testing tested) same selectable adj power adj supplies	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:49
S45	12	S38 and (test testing tested) with (prior before) adj fabrication	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:54
S46	328	S38 and wafer adj test	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:54
S47	3	S38 and wafer adj test with circuit adj block	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/14 14:38

S48	1	S38 and wafer adj test with (fabricate fabricated fabrication) with (complete completed completing)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 17:58
S49	6	S38 and test with (prior before) with (fabricate fabricated fabrication) with (complete completed completing)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 18:03
S50	0	S38 and common adj external adj terminal with wire adj bonding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 18:04
S51	0	S38 and common adj terminal with wire adj bonding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 18:04
S52	6	S38 and power near2 terminal with wire adj bonding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 18:13
S53	4	S38 and supply adj terminal with wire adj bonding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 18:14
S54	9	S38 and supply adj terminal same wire adj bonding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 18:17
S55	31	S38 and third adj circuit adj block	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/02/11 18:17